

Two-Dimensional Xenex: Synthesis, Processing, and Engineering

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Isolation of graphene paves the way to a new and unprecedentedly rich fashion of two-dimensional (2D) materials. While many of them are naturally available in the form of exfoliable single-crystal flakes, others can be artificially derived by synthetic approaches and their atomistic features tailored by design. Xenex, namely 2D single-element materials beyond graphene, are a representative case in this respect [1]. Xenex expand the graphene potential and constitute an emerging nanomaterials platform with potential for ultra-scaled electronics, topological science, photonics, energetics, and biomedicine [2].

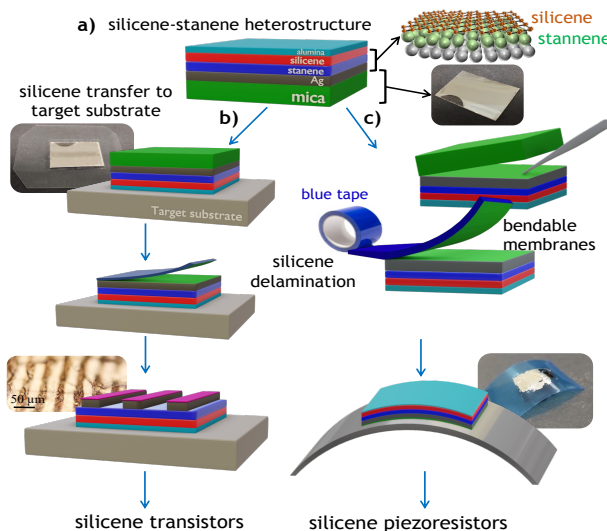


Fig. 1 Processing of silicene-stannene heterostructures (a) for the manufacture of silicene transistors (b) and piezoresistors (c).

Here we will present a taxonomy of the Xenex with element X spanning from the IV column of the periodic table (e.g. silicene) to the adjacent columns (e.g. borophene, phosphorene, tellurene, etc.), and Xenex configurations like functionalized Xenex, Xenex hybrids, and Xenex heterostructures. In parallel, with silicene, stannene, and tellurene as representative examples, we will describe how the Xenex can be produced by epitaxial methods including the direct deposition, the top-to-bottom intercalation through an interface layer, the bottom-to-top segregation, and chemical methods like topotactic deintercalation. In particular, silicene stands out as a paradigmatic case for atomic structure engineering. In this respect, interface engineering through atomic Sn decoration and stannene buffering leads to the scalable

epitaxy of phase-selected silicene [3] and to the formation of Xenex heterostructures [4], respectively. Xenex heterostructures with a top face Al_2O_3 capping layer are the enabling layout for a durable stabilization of silicene against environmental degradation via an all-around encapsulation (Fig. 1a) [5]. This configuration allows us to elucidate the inherent electrical, thermal, and optical properties of silicene with no interference from the substrate. Stabilized silicene membranes can be thus delaminated off from the pristine substrates and transferred to second target substrate (Fig. 1b) aiming at electronic device applications [6]. We will describe methods leading us to fabricate silicene transistors on solid-state platforms and a silicene piezoresistor on bendable substrate (Fig. 1c) [7].

The development of silicene by means of epitaxial methods and atomic control at the surface level thus proves to be a viable path for Xenex engineering towards the derivation of unique properties in carefully tailored configurations and the manufacture of prototypical devices. This approach can be universally extended to the whole class of the epitaxial Xenex towards a Xenex-based science and technology.

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References

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